



SOLVE

The Virtual Lab @ NITK Surathkal, Funded by NME- ICT, MHRD
CENTRE FOR SYSTEM DESIGN
NATIONAL INSTITUTE OF TECHNOLOGY KARNATAKA SURATHKAL
P.O. SRINIVASNAGAR, MANGALORE – 575 025

NITK/SOLVE/KVG/PU/2016/151

Date: 28-07-2016

ADVERTISEMENT

Applications are invited from interested candidates for the post of **Junior Research Fellow(2 No.s)** in the **Virtual Lab Phase-2 project** . The appointment will be purely temporary for a period maximum upto March 31, 2017.

Sl. No.	Position	Desired Qualification	No. of Posts	Consolidated pay (In Rs. / Month) *	Duration of Contractual Period
1	Junior Research Fellow	BE/B.Tech in Mechanical Engineering(minimum 75%:aggregate of last 6 semesters) with experience in Solid Edge, Ansys, CNC Part Programming, Virtual Instrumentation, Product manufacturing	01	Rs.16,000/	Maximum upto March 31 , 2017
2	Junior Research Fellow	BE/B.Tech in Electrical & Electronics(minimum 75%:aggregate of last 6 semesters) with experience in Virtual Instrumentation using labview, active vibration control system, Mathematical modeling	01	Rs.16,000/	Maximum upto March 31 , 2017

* Consolidated pay with no accommodation facility

Interested candidates for Junior Research Fellow should be present for software proficiency test (practical)** and a technical interview on 09th August 2016 at NITK Surathkal. Report at 9 am at Center for System Design, NITK Surathkal with applications along with passport size photo on plain paper, original certificates and a self attested copy of original certificates. Candidates must bring laptops for the software proficiency test.

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- Candidates with Mechanical Engineering Background, the software proficiency test includes modeling in solid edge analysis in ANSYS and Data acquisition and simulation by using Labview etc.
- Candidates with Electrical Engineering Background, the software proficiency test includes Data acquisition by using NI DAQ and Labview along with Mathematical modeling of active vibration control system etc.

Contact Person:

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